SPECIFICATION

Customer: _____

Model Name: SAT101CP50D24Y0-501431

SPEC NO.: _____

Date: _____

Version: _____

■ Preliminary Specification

□ Final Specification

Approved by	Comment

Approved by	Reviewed by	Prepared by
视安通 2014.09.01 吴金荣		

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1. General Specifications

No.	Item	Specification	Remark
1	LCD size	10.1 inch(Diagonal)	
2	Driver element	a-Si TFT active matrix	
3	Resolution	1024 × 3(RGB) ×600	
4	Display mode	Normally White, Transmissive	
5	Pixel pitch	0.2175(H) X 0.2088(V) X RGB mm	
6	Active area	222.72(H) X 125.28(V) mm	
7	Outline dimensions	235(H) X 143(V) X 5.0(D) mm	
8	Surface treatment	Anti-Glare	
9	Color arrangement	RGB-stripe	
10	Interface	TTL RGB-24bit parallel interface	
11	Backlight Power consumption	TBD	
12	Panel Power consumption	TBD	
13	Weight	TBD	

2. Pin Assignment

FPC Connector is used for the module electronics interface. The recommended model is FH12A-50S-0.5SH manufactured by Hirose.

Pin No.	Symbol	I/O	Function	Remark
1	LED+	P	Power for LED backlight(anode)	
2	LED+	P	Power for LED backlight(anode)	
3	LED-	P	Power for LED backlight(Cathode)	
4	LED-	P	Power for LED backlight(Cathode)	
5	GND	P	Power ground	
6	V_{COM}	I	Common voltage	
7	DVDD	P	Power for Digital Circuit	
8	MODE	I	DE/SYNC mode select	Note 1
9	DE	I	Data Input Enable	
10	VS	I	Vertical Sync Input	
11	HS	I	Horizontal Sync Input	
12	B7	I	Blue data(MSB)	
13	B6	I	Blue data	
14	B5	I	Blue data	
15	B4	I	Blue data	
16	B3	I	Blue data	
17	B2	I	Blue data	
18	B1	I	Blue data	Note 2
19	B0	I	Blue data(LSB)	Note 2
20	G7	I	Green data(MSB)	
21	G6	I	Green data	
22	G5	I	Green data	
23	G4	I	Green data	
24	G3	I	Green data	
25	G2	I	Green data	
26	G1	I	Green data	Note 2

27	G0	I	Green data(LSB)	Note 2
28	R7	I	Red data(MSB)	
29	R6	I	Red data	
30	R5	I	Red data	
31	R4	I	Red data	
32	R3	I	Red data	
33	R2	I	Red data	
34	R1	I	Red data	Note 2
35	R0	I	Red data(LSB)	Note 2
36	GND	P	Power Ground	
37	DCLK	I	Sample clock	Note 3
38	GND	P	Power Ground	
39	L/R	I	Left / right selection	Note 4,5
40	U/D	I	Up/down selection	Note 4,5
41	V_{GH}	P	Gate ON Voltage	
42	V_{GL}	P	Gate OFF Voltage	
43	AV_{DD}	P	Power for Analog Circuit	
44	RESET	I	Global reset pin.	Note 6
45	NC	-	No connection	
46	V_{COM}	I	Common Voltage	
47	DITHB	I	Dithering function	Note 7
48	GND	P	Power Ground	
49	NC	-	No connection	
50	NC	-	No connection	

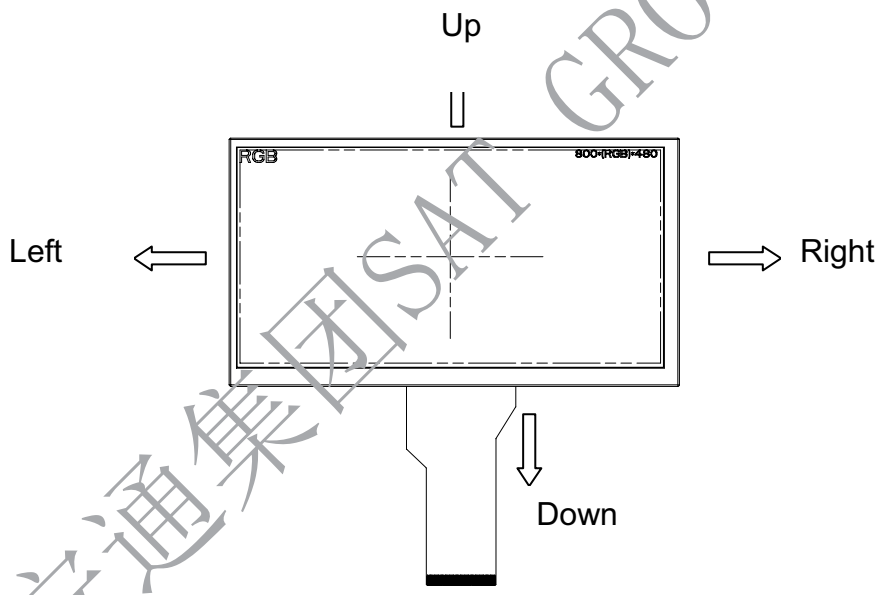
I: input, O: output, P: Power

Note 1: DE/SYNC mode select. Normally pull high.
When select DE mode, MODE="1", VS and HS must pull high.
When select SYNC mode, MODE= "0", DE must be grounded.
Note 2: When input 18 bits RGB data, the two low bits of R,G and B data must be grounded.
Note 3: Data shall be latched at the falling edge of DCLK.

Note 4: Selection of scanning mode

Setting of scan control input		Scanning direction
U/D	L/R	
GND	DV_{DD}	Up to down, left to right
DV_{DD}	GND	Down to up, right to left
GND	GND	Up to down, right to left
DV_{DD}	DV_{DD}	Down to up, left to right

Note 5: Definition of scanning direction.
Refer to the figure as below:



Note 6: Global reset pin. Active low to enter reset state. Suggest to connect with an RC reset circuit for stability. Normally pull high.

Note 7: Dithering function enable control, normally pull high.
When DITHB="1",Disable internal dithering function,
When DITHB="0",Enable internal dithering function,

3. Operation Specifications

3.1. Absolute Maximum Ratings

(Note 1)

Item	Symbol	Values		Unit	Remark
		Min.	Max.		
Power voltage	DV_{DD}	-0.3	3.96	V	
	AV_{DD}	-0.5	14.85	V	
	V_{GH}	-0.3	40.0	V	
	V_{GL}	-20.0	0.3	V	
	V_{GH}-V_{GL}	-	40.0	V	
Operation Temperature	T_{OP}	-20	70	℃	
Storage Temperature	T_{ST}	-30	80	℃	

Note 1: The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

3.1.1. Typical Operation Conditions

(Note 1)

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Power voltage	DV_{DD}	3.0	3.3	3.6	V	Note 2
	AV_{DD}	9.4	9.6	9.8	V	
	V_{GH}	17	18	19	V	
	V_{GL}	-6.6	-6.0	-5.4	V	
Input signal voltage	V_{COM}	3.1	3.3	3.5	V	Note 4
Input logic high voltage	V_{IH}	0.7 DV_{DD}	-	DV_{DD}	V	Note 3
Input logic low voltage	V_{IL}	0	-	0.3 DV_{DD}	V	

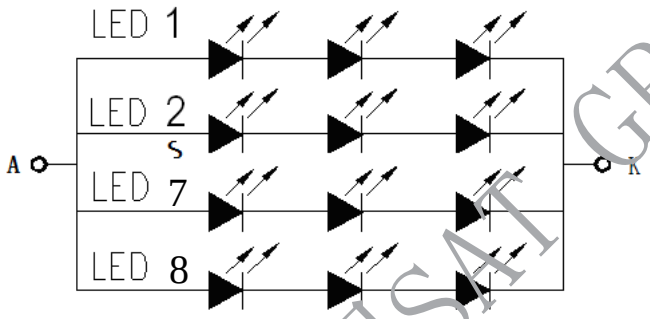
Note 1: Be sure to apply DV_{DD} and V_{GL} to the LCD first, and then apply V_{GH}.
Note 2: DV_{DD} setting should match the signals output voltage (refer to Note 3) of customer's system board.
Note 3: DCLK,HS,VS,RESET,U/D, L/R,DE,R0~R7,G0~G7,B0~B7,MODE,DITHB.
Note 4: Please adjust VCOM to make the flicker level be minimum.

3.1.2. Current Consumption

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Current for Driver	I_{GH}	-	0.5	1.0	mA	V_{GH} =18V
	I_{GL}	-	0.5	1.0	mA	V_{GL} =-6V
	IDV_{DD}	-	40	50	mA	DV_{DD} =3.3V
	IAV_{DD}	-	35	45	mA	AV_{DD} =9.6V

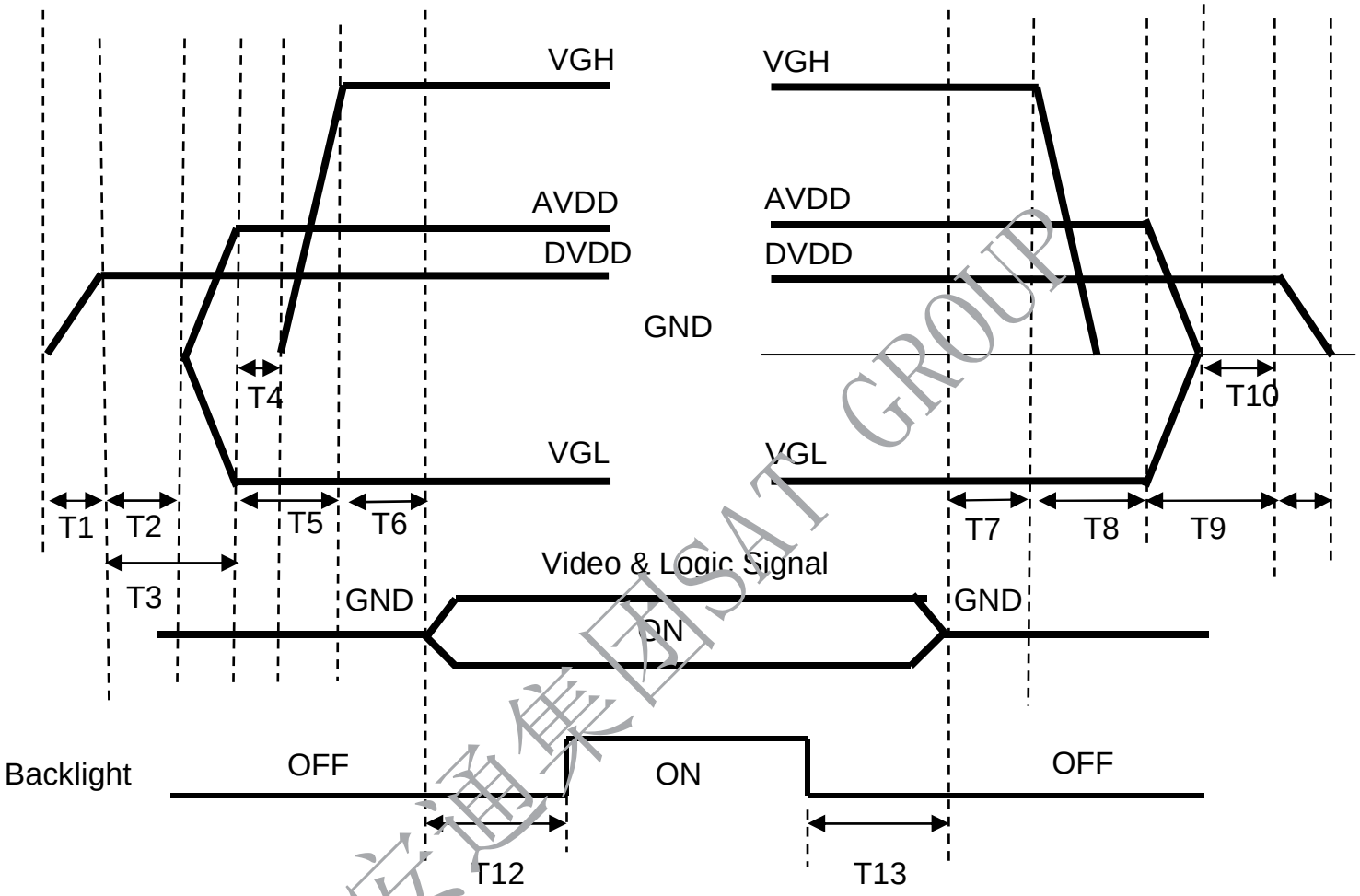
3.1.3. Backlight Driving Conditions (24 White Chips)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Note
Supply voltage of white LED backlight	VL	8.7	9.6	10.5	V	Note 1
Current for LED backlight	IL	120	160	200	mA	
Luminance (on the module surface,BM-7)		110	160	-	cd/m^2	
LED life time	-	20,000	-	-	Hr	Note 2



3.2. Power Sequence

Power On：DVDD→AVDD/VGL →VGH →Video &Logic Signal→Backlight
Power Off：Backlight→Video &Logic Signal→ VGH→AVDD/VGL→DVDD



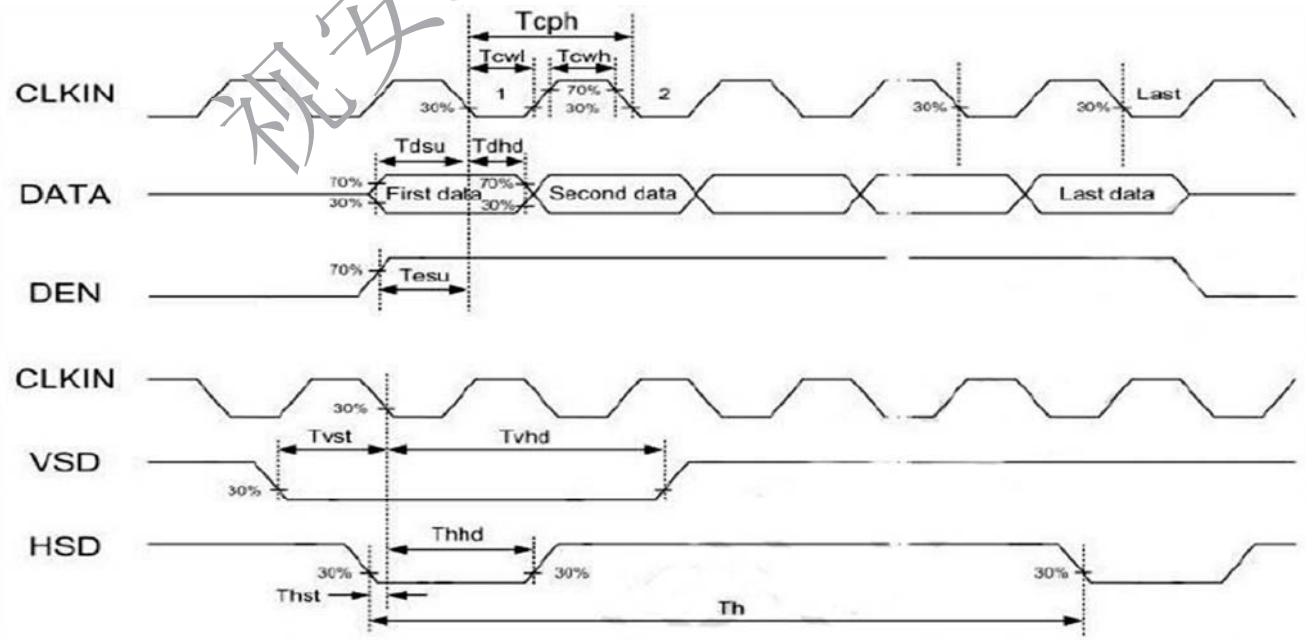
0<T1≦10ms
T2>0ms
T3>20ms
T4>0ms
T5>10ms
0<T6≦10ms
T12≧200ms

T7>0ms
T8>0ms
T9>0ms
T10>0ms
0<T11≦10ms
T13≧200ms

3.3. Timing Characteristics

3.3.1. AC Electrical Characteristics

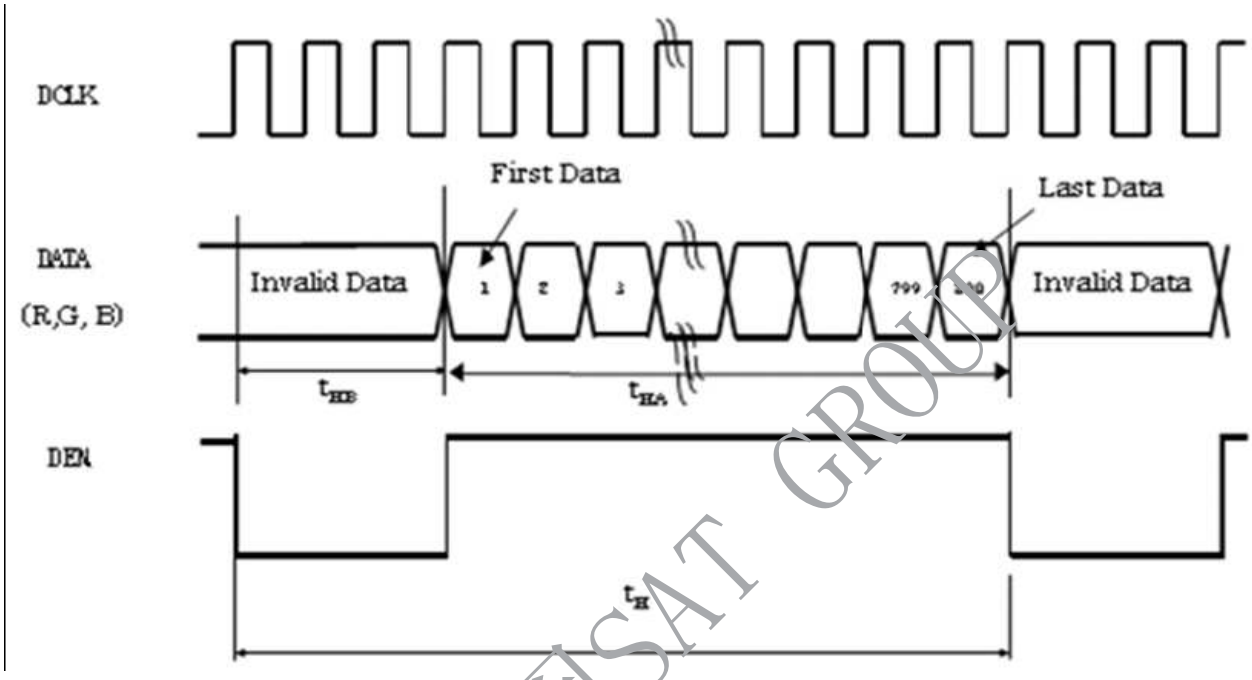
ITEM		SYMBOL	MIN.	TYP.	MAX.	UNIT	Note
DCLK	Dot Clock	$1/t_{CLK}$	45	51.2	57	MHz	
	DCLK pulse duty	Tcwh	40	50	60	%	
DE	Setup Time	Tesu	5	–	–	ns	
	Hold time	Tehd	5	–	–	ns	
	Horizontal total Time	t_H	1324	1344	1364	t_{CLK}	
	Horizontal Valid	t_{HA}		1024		t_{CLK}	
	Horizontal Blank	t_{HB}	300	320	340	t_{CLK}	
	Vertical total Time	t_V	625	635	645	t_H	
	Vertical Valid	t_{VA}		600		t_H	
	Vertical Blank	t_{VB}	25	35	45	t_H	
SYNC	HSYNC Setup Time	Thst	5	–	–	ns	
	HSYNC Hold Time	Thhd	5	–	–	ns	
	VSYNC Setup Time	Tvst	5	–	–	ns	
	VSYNC Hold Time	Tvhd	5	–	–	ns	
	Horizontal total Time	th	1324	1344	1364	t_{CLK}	
	Horizontal Pulse Width	thpw		20	–	tCLK	thb + thpw=160DC
	Horizontal Back Porch	thb		140	–	tCLK	
	Horizontal Front Porch	thfp	140	160	180	tCLK	
	Horizontal Valid	thd		1024		tCLK	
	Vertical total Time	tv	625	635	645	th	
	Vertical Pulse Width	tvpw		3	–	th	tvpw + tvb = 23th is fixed
	Vertical Back Porch	tvb	–	20	–	th	
	Vertical Front Porch	tvfp	2	12	22	th	
	Vertical Valid	tvd		600		th	
DATA	Setup Time	Tdsu	5	–	–	ns	
	Hold Time	Tdhd	5	–	–	ns	



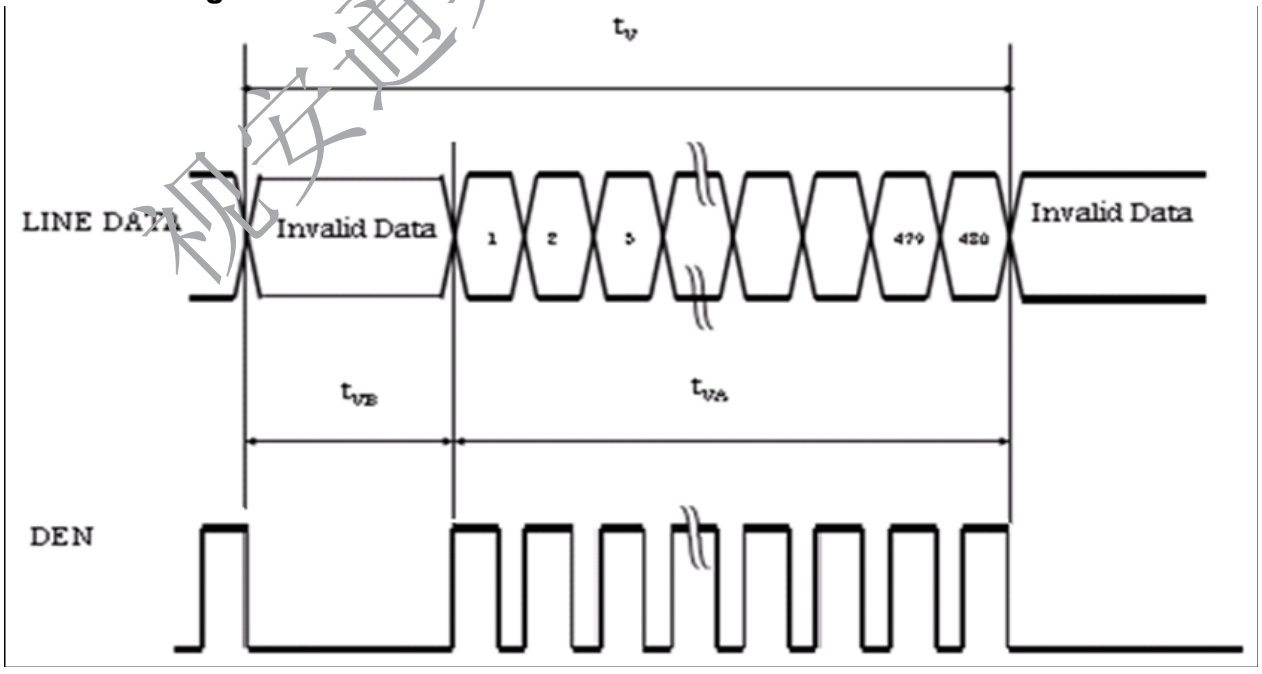
3.3.2 Timing Sequence(Timing Chart)

DE mode

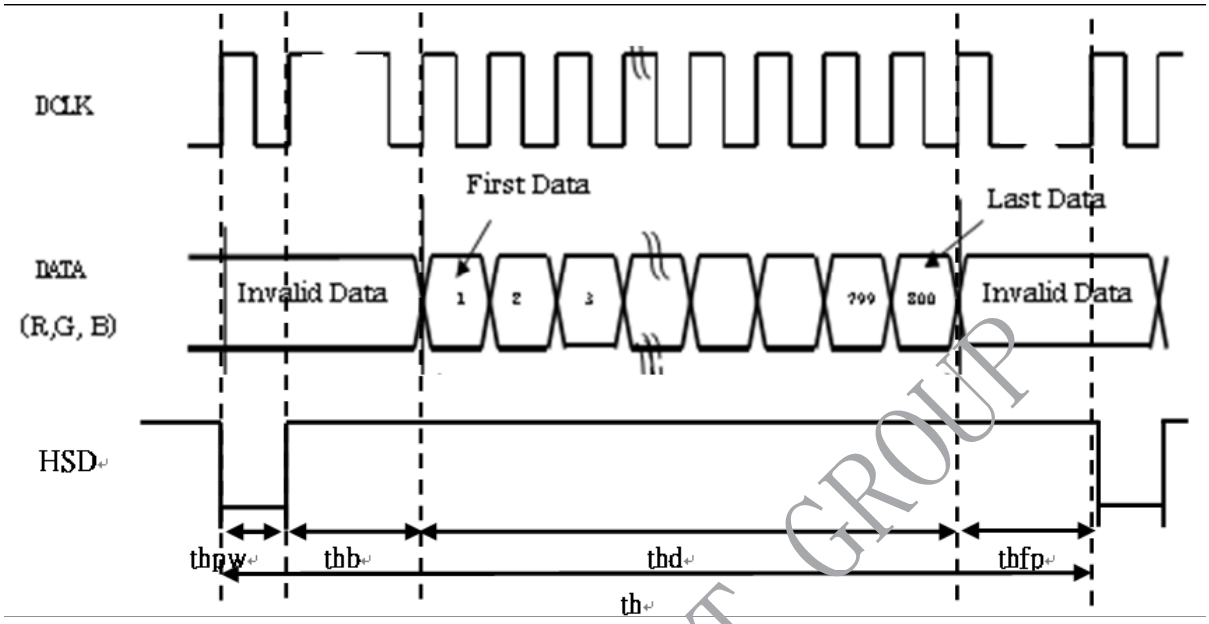
Horizontal timing



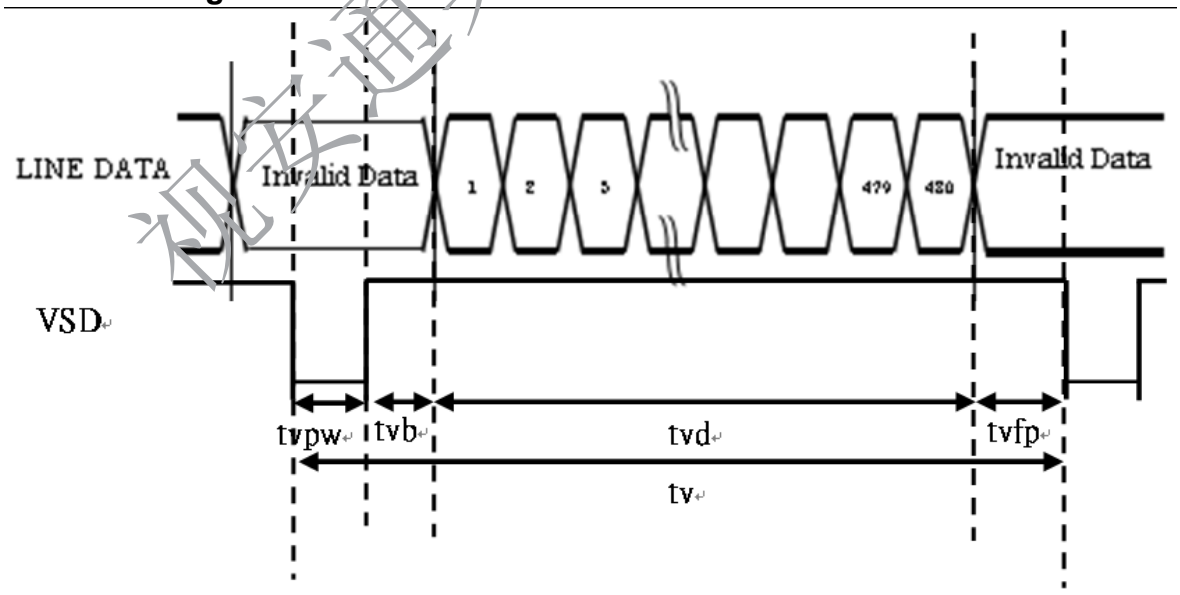
Vertical timing：



SYNC mode
Horizontal timing：



Vertical timing：



4. Optical Specifications

Item		Symbol	Condition	Min	Typ	Max	Unit	Remark
View Angles		θT	CR≧10	60	70	--	Degree	Note1
		θB		40	50	--		
		θL		60	70	--		
		θR		60	70	--		
Contrast Ratio		CR	θ=0°	500	600	--		Note4
Response Time		$T_{ON}+T_{OFF}$	25℃	--	25	40	ms	Note3
Chromaticity	White	x	Backlight is on	0.273	0.313	0.353		Note2 Note5 Note6
		y		0.298	0.329	0.369		
	Red	x		0.550	0.590	0.630		
		y		0.293	0.333	0.373		
	Green	x		0.301	0.341	0.381		
		y		0.549	0.589	0.629		
	Blue	x		0.122	0.162	0.202		
		y		0.059	0.099	0.139		
Uniformity		U		70	80	--	%	Note7
NTSC				--	50	--	%	
Luminance		L		110	160	--	cd/m^2	Note6

Test Conditions:

1. DV_{DD}=3.3V, I_L = 160mA(Backlight current),the ambient temperature is 25℃.
2. The test systems refer to Note 2.

Note 1: Definition of viewing angle range

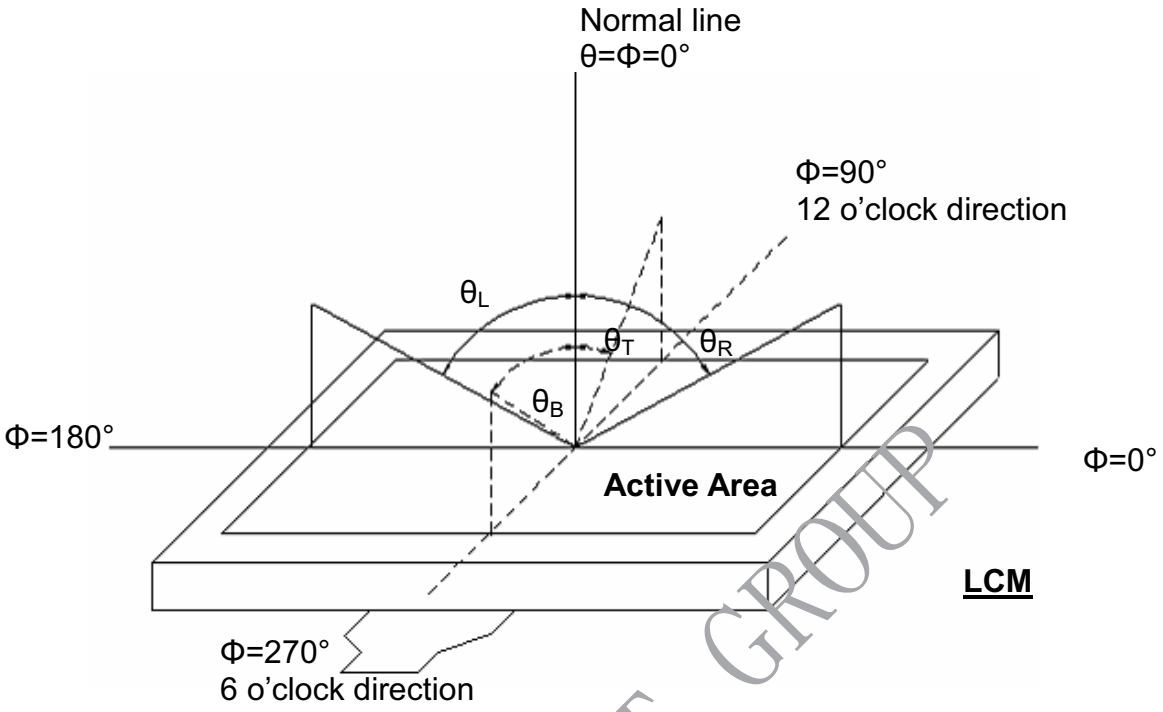


Fig. 4-1 Definition of viewing angle

Note 2: Definition of optical measurement system.
The optical characteristics should be measured in dark room. After 30 minutes operation, the optical properties are measured at the center point of the LCD screen. (Response time is measured by Photo detector TOPCON BM-7, other items are measured by BM-5A/Field of view: 1° /Height: 500mm.)

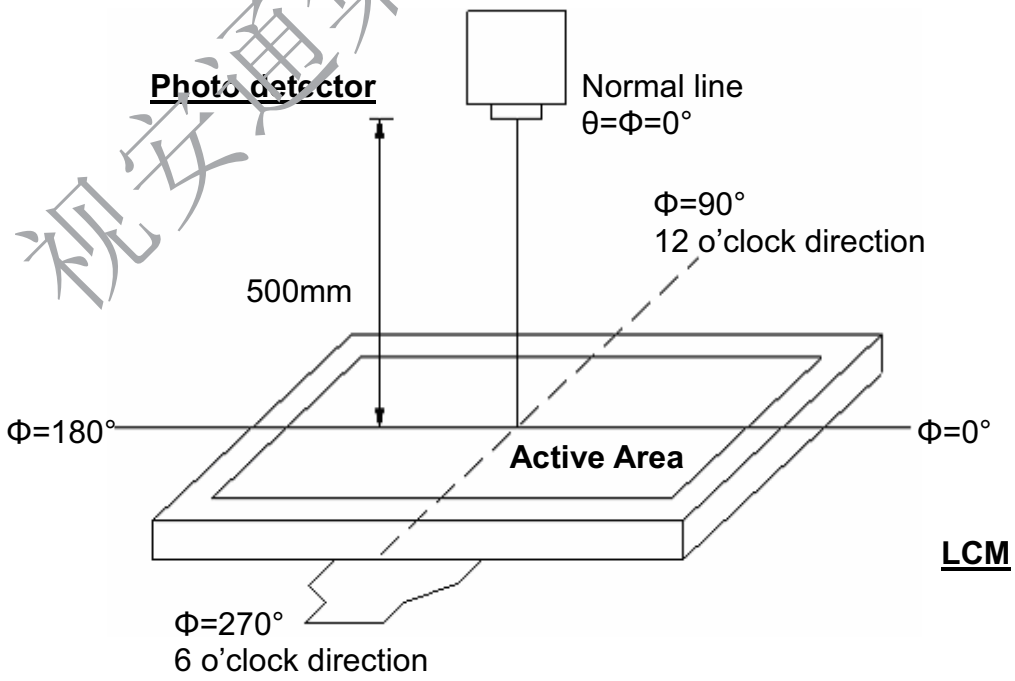


Fig. 4-2 Optical measurement system setup

Note 3: Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%.

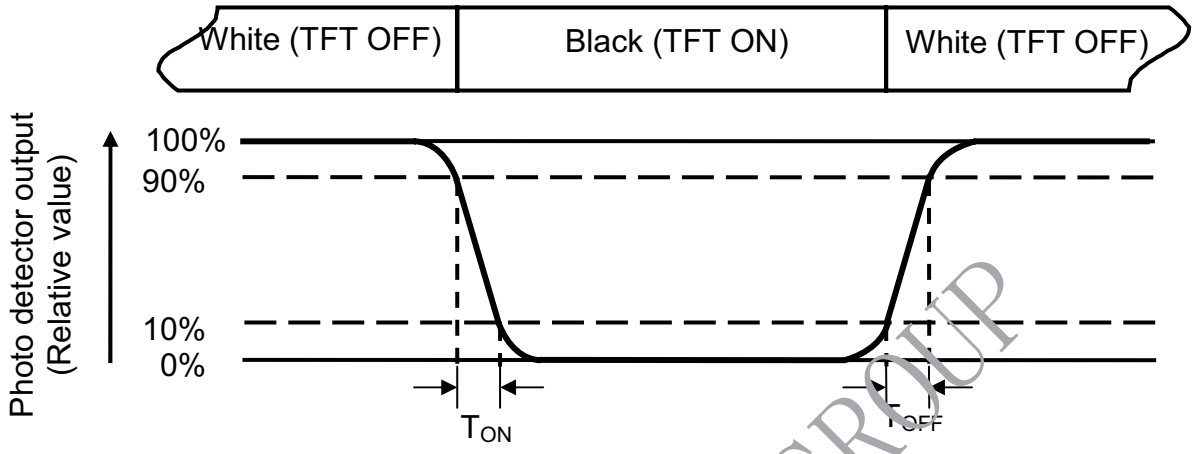


Fig. 4-3 Definition of response time

Note 4: Definition of contrast ratio

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

Note 5: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

Note 6: All input terminals LCD panel must be ground while measuring the center area of the panel.

Note 7: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer to Fig. 4-4).Every measuring point is placed at the center of each measuring area.

$$Luminance\ \ Uniformity\ (Yu) = \frac{B_{min}}{B_{max}}$$

L-------Active area length W----- Active area width

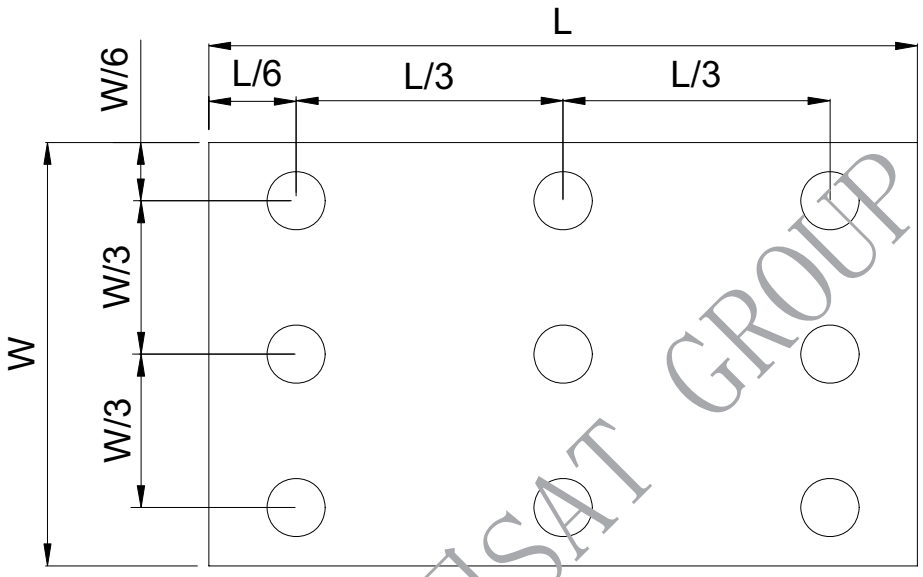


Fig. 4-4 Definition of measuring points

B_{max}: The measured maximum luminance of all measurement position.
B_{min}: The measured minimum luminance of all measurement position.

5. Reliability Test Items

(Note3)

Item	Test Conditions	Remark
High Temperature Storage	Ta = 80℃ 240hrs	Note 1，Note 4
Low Temperature Storage	Ta = -30℃ 240hrs	Note 1，Note 4
High Temperature Operation	Ts = 70℃ 240hrs	Note 2，Note 4
Low Temperature Operation	Ta = -20℃ 240hrs	Note 1，Note 4
Operate at High Temperature and Humidity	+60℃, 90%RH 240hrs	Note 4
Thermal Shock	-30℃/30 min ~ +80℃/30 min for a total 100 cycles, Start with cold temperature and end with high temperature.	Note 4
Vibration Test	Frequency range:10~55Hz Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2 hours for each direction of X. Y. Z. (6 hours for total)	
Mechanical Shock	100G 6ms,±X, ±Y, ±Z 3 times for each direction	
Package Vibration Test	Random Vibration : 0.015G*G/Hz from 5-200HZ, -6dB/Octave from 200-500HZ 2 hours for each direction of X. Y. Z. (6 hours for total)	
Package Drop Test	Height:60 cm 1 corner, 3 edges, 6 surfaces	
Electro Static Discharge	± 2KV, Human Body Mode, 100pF/1500Ω	

Note 1: Ta is the ambient temperature of samples.
Note 2: Ts is the temperature of panel's surface.
Note 3: In the standard condition, there shall be no practical problem that may affect the display function. After the reliability test, the product only guarantees operation, but don't guarantee all of the cosmetic specification.
Note 4: Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.

6. General Precautions

6.1. Safety

Liquid crystal is poisonous. Do not put it in your mouth. If liquid crystal touches your skin or clothes, wash it off immediately by using soap and water.

6.2. Handling

1. The LCD panel is plate glass. Do not subject the panel to mechanical shock or to excessive force on its surface.
2. The polarizer attached to the display is easily damaged. Please handle it carefully to avoid scratch or other damages.
3. To avoid contamination on the display surface, do not touch the module surface with bare hands.
4. Keep a space so that the LCD panels do not touch other components.
5. Put cover board such as acrylic board on the surface of LCD panel to protect panel from damages.
6. Transparent electrodes may be disconnected if you use the LCD panel under environmental conditions where the condensation of dew occurs.
7. Do not leave module in direct sunlight to avoid malfunction of the ICs.

6.3. Static Electricity

1. Be sure to ground module before turning on power or operating module.
2. Do not apply voltage which exceeds the absolute maximum rating value.

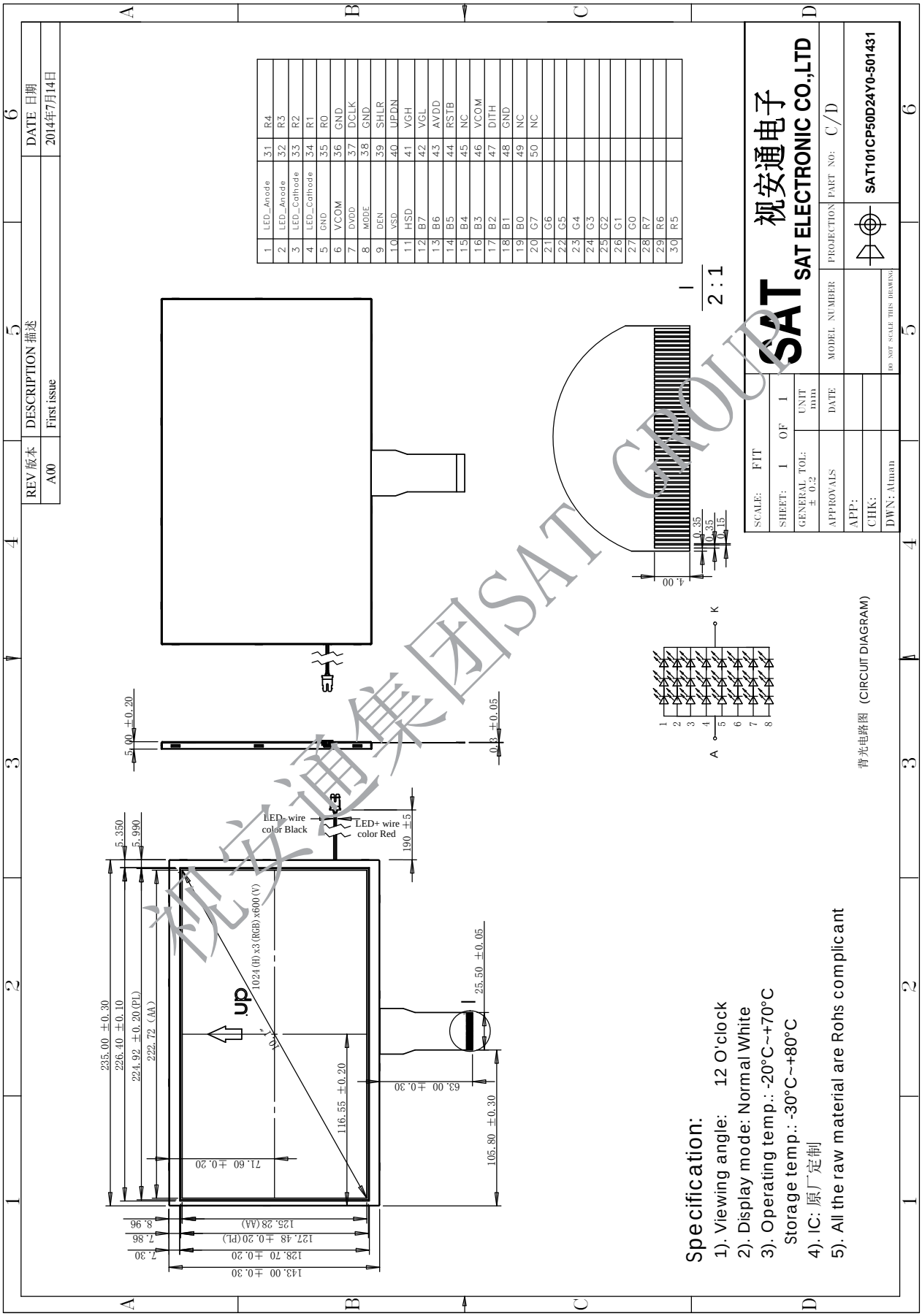
6.4. Storage

1. Store the module in a dark room where must keep at 25±10℃ and 65%RH or less.
2. Do not store the module in surroundings containing organic solvent or corrosive gas.
3. Store the module in an anti-electrostatic container or bag.

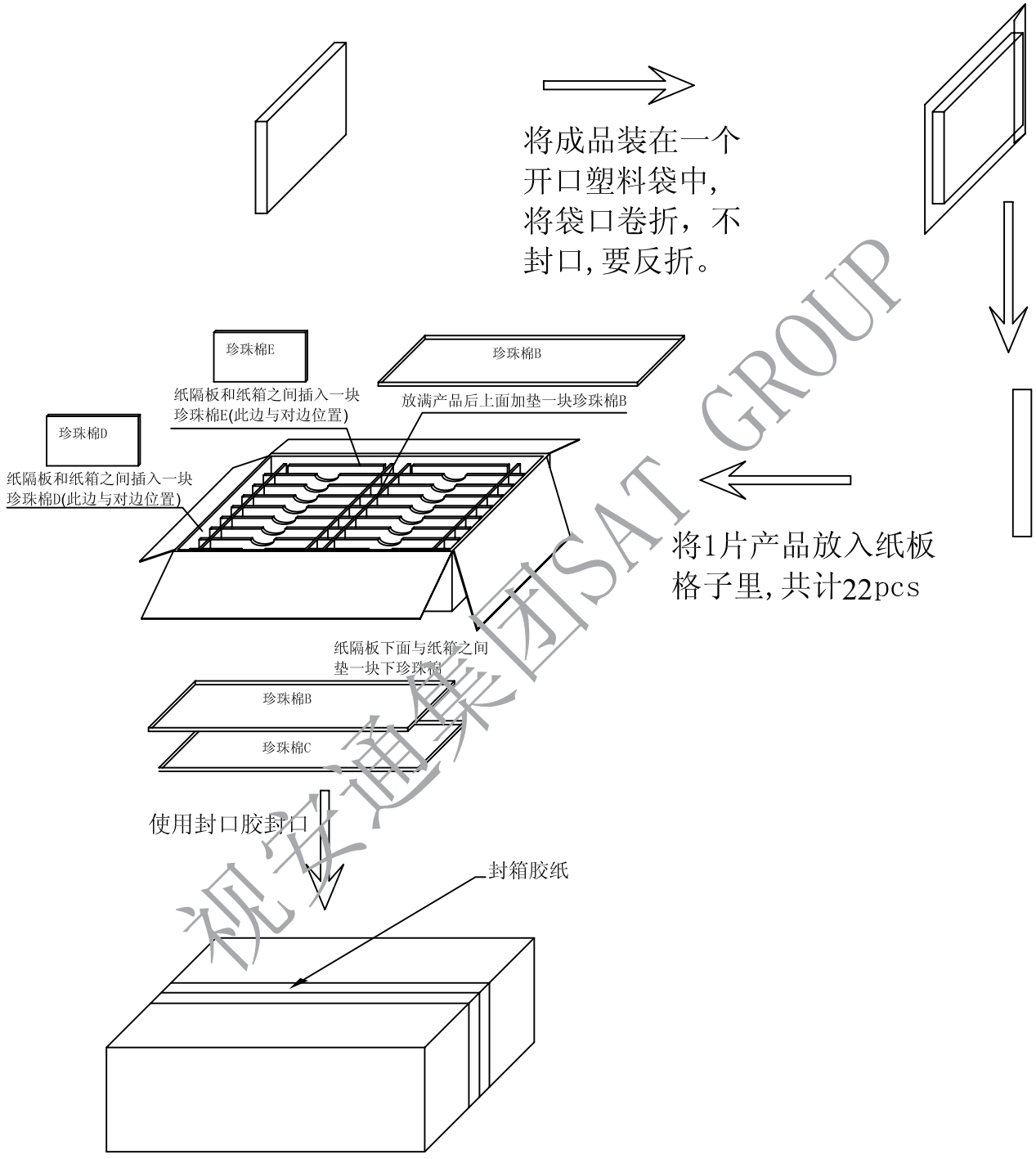
6.5. Cleaning

1. Do not wipe the polarizer with dry cloth. It might cause scratch.
2. Only use a soft sloth with IPA to wipe the polarizer, other chemicals might permanent damage to the polarizer.

7. Mechanical Drawing

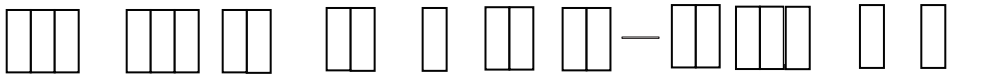


8. Package Drawing



9 . Product ID Rule

Product Name



No	Definition	Specifications
(1)	TFT LCM Productor No.	**SAT**---- **SAT ELECTRONIC CO.LTD**
(2)	Display monitor opposite angle line size	Unit :inch or mmm (size <10 inch: takes two integers ; size >=10 inch: takes three integers)
(3)	LCD Type	AU----AUO ; CP----CPT ; PV----PVI ; TM----TIANMA ; HS----HSD ; LG----LG ; Wi----Wintek ; CM----CMO ; HY----Hydis ; HI----Hitach; Sh----Sharp ; BO---BOE 。。。
(4)	Interface PIN Number	By two figures characters expression from 01 to 99
(5)	Interface Signal Type	A---- Alternated Video Signal; D---- Data Video Signal; H----HDV; I----IPS
(6)	LED backlight Lamp Number	By two figures characters expression from 01 to 99
(7)	LED Back Light colored warp	Rx----red ; Gx----green ; Bx---- blueness; Yx----white; P----PVI; x---- warp distinction,1 minimal,9 maximal
(8)	Thickness	By two figures characters expression from 01 to 99

No	Definition	Specifications
(9)	Width	By three figures characters expression from 001 to 999
(10)	Product information	By one figure characters expression from 0 to 9
(11)	FPC Type	S---short M---Middle L---Long